

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	31466	(substrate wafer) near5 (groove recess)	US-PGPU B; USPAT	OR	ON	2005/03/16 16:47
L2	4005	1 and die	US-PGPU B; USPAT	OR	ON	2005/03/16 16:47
L4	271	2 and encapsulant	US-PGPU B; USPAT; USOCR	OR	ON	2005/03/16 16:48
L5	35197	(substrate wafer) near5 (groove recess)	USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/16 16:47
L6	534	5 and die	USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/16 16:48
L7	7	6 and encapsulant	USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2005/03/16 16:48